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Reviews

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(Dr. Teagan Beahan Sr.)

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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 16384 Publisher: Tsinghua University Press Pub. Date :2009-05. This book draws on the most classic of relevant domestic and foreign materials. systematic exposition of the basic concepts of computer system structure. basic structure. basic methods. emphasizing the parallelism from the development point of view. with quantitative analysis (Performance Evaluation) approach to research and analyze computer systems. A total of 13 book chapters. Chapter 1 introduces the basic concepts of parallel development and quantitative analysis of the foundation. Chapter 2 describes the design of computer instruction and MIPS instruction set. Chapter 3 introduces the assembly line technology. including performance analysis. scheduling. and conflict-related and pipeline implementation. Chapter 4 describes the structure of the vector processor. features. and performance evaluation. Chapters 5 and 6. respectively. on how to use hardware and software methods to develop the instruction-level parallelism. including the scoreboard and Tomasulo algorithms. hardware-based implementation of forward-looking. multi-flow technology instruction. loop unrolling. VLIW. EPIC and so on. Chapter 7 describes the storage level. focuses on the basic knowledge and Cache Cache to improve the performance of various methods. Chapter 8 describes the bus. the channel processor and RAID. Chapter 9 describes the characteristics of the interconnection network parameters. static Four Satisfaction guaranteed, or money back.



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