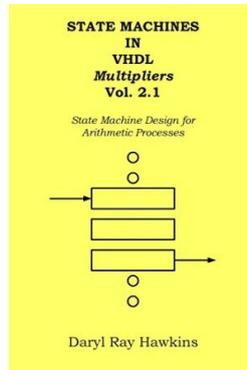


State Machines in VHDL Multipliers Vol. 2: State Machine Design for Arithmetic Processes



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